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APPLICANTS

Jose De Jesus Pineda de Gyvez, Eindhoven, NETHERLANDS;
 Alexander Guido Gronthoud, Eindhoven, NETHERLANDS;
 Cristiano Cenci, Castel De Piano, ITALY;

** CONTINUING DATA *****

This application is a 371 of PCT/IB05/50343 01/27/2005 *O.K.*

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 04100593.5 02/13/2004 *O.K.*

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No		NETHERLANDS	6	18	2

ADDRESS

NXP, B.V.
 NXP INTELLECTUAL PROPERTY DEPARTMENT
 M/S41-SJ
 1109 MCKAY DRIVE
 SAN JOSE, CA 95131
 UNITED STATES

TITLE

Method and device for testing a phase locked loop

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